

IN THE CLAIMS

1. (Currently Amended) A method of multiple description channel coding of video data using forward error correction, the method comprising the steps of:

in a video processing system,

receiving input video data;

determining DCT coefficients for the video data;

coding the DCT coefficients into a base layer bitstream and an enhancement layer bitstream according to a fine-granular scalability coding;

converting the base layer bitstream and the enhancement layer bitstream into a plurality of equal priority descriptions; and

allowing a fractional number of descriptions ~~wherein a plurality of equal priority partitions is comprised~~by using of partitions generated from the base and enhancement layer bitstreams and a forward error correction (FEC) code according to predetermined criteria ~~and allowing a fractional number of descriptions.~~

2. (Previously Presented) The method according to Claim 1, further comprising the step of transmitting the plurality of equal priority descriptions over different transmission channels.

3. (Original) The method according to Claim 1, further comprising the step of decoding the plurality of equal priority descriptions.

4. (Original) The method according to Claim 3, wherein the decoding step is performed based on at least one of the plurality of equal priority descriptions.
5. Cancelled.
6. (Currently Amended) An apparatus for coding an input video comprising:
 - a memory which stores computer-executable process steps; and
 - a processor which executes the process steps stored in the memory so as (i) receive a base layer and an enhancement layer that include an input video data encoded according to a fine-granular scalability coding, (ii) to convert the base layer and the enhancement layer into a plurality of equal priority descriptions, (iii) to transmit the converted equal priority descriptions over different transmission channels, **allowing a fractional number of descriptions** ~~wherein a plurality of equal priority partitions is comprised~~ **by using** of partitions generated from the base and enhancement layer bitstreams and a forward error correction (FEC) code according to predetermined criteria ~~and allowing a fractional number of descriptions~~.
7. (Previously Presented) The apparatus according to Claim 6, further comprises means for decoding at least one of the plurality of equal priority descriptions wherein the reconstructed video is drift-free as long as the decoding means receives at least one plurality of equal priority descriptions.
8. (Original) The apparatus according to Claim 7, wherein the decoding means is an MPEG-4 decoder.

9. Cancelled.

10. (Original) The apparatus according to Claim 6, wherein the plurality of equal priority partitions is generated from the base and enhancement layers and a forward error correction (FEC) code.

11. (Currently Amended) A system for processing an input video data, the apparatus comprising:

means for determining DCT coefficients of the input video data;

means for coding the DCT coefficients into a base layer and an enhancement layer that include the input video data according to a fine-granular scalability coding; and

means for converting the base layer and the enhancement layer into a plurality of equal priority descriptions; and

allowing a fractional number of descriptions ~~wherein a plurality of equal priority partitions is comprised~~ **by using** of partitions generated from the base and enhancement layer bitstreams and a forward error correction (FEC) code according to predetermined criteria ~~and allowing a fractional number of descriptions.~~

12. (Original) The system according to Claim 11, further comprising means for transmitting at least one of the plurality of equal priority descriptions layers over different transmission channels.

13. (Original) The system according to Claim 11, further comprising means for decoding at least one of the plurality of equal priority descriptions.

14. Cancelled.

15. (Original) The system according to Claim 13, wherein the decoding means is an MPEG-4 decoder.

16. (Previously Presented) The method according to Claim 1, wherein the equal-priority partitions are generated by alternatively skipping the bit plane for certain blocks with the partitions being orthogonal to each other and having equal priority.